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# CLAIMS

What is claimed is:

1. ~~A method, comprising:~~  
2 providing a first resistor with a first end and a second end, said  
3 first end coupled to a switch and said second end coupled to  
4 a data bus wire;  
5 controlling said switch with a detach control signal; and  
6 ~~switching a biasing voltage from said resistor utilizing said switch.~~

2. ~~The method of claim 1, wherein said first resistor is~~  
2 ~~configured as a pull-down resistor.~~

3. The method of claim 1, wherein said first resistor is  
2 configured as a pull-up resistor.

4. The method of claim 3, further comprising detecting said  
2 switching of said biasing voltage.

5. The method of claim 4, further comprising determining a  
2 logically detached state responsive to said detecting.

6. The method of claim 1, wherein said detach control signal is  
2 responsive to a wake-up signal.

7. The method of claim 6, wherein said detach control signal is  
2 asserted when said wake-up signal is de-asserted.

1        8.    An apparatus, comprising:  
 2        a first resistor with a first end and a second end;  
 3        a switch coupled to said first end of said first resistor and to a bias  
 4        voltage;  
 5        a detach control signal wire coupled to said switch; and  
 6        a data bus wire coupled to said second end of said first resistor.

1        9.    The apparatus of claim 8, wherein said switch may apply  
 2        said bias voltage to said first end of said first resistor responsively to a  
 3        detach control signal on said detach control signal wire.

1        10.   The apparatus of claim 9, wherein said detach control signal  
 2        is generated responsively to a wake-up signal.

1        11.   The apparatus of claim 8, wherein said data bus wire carries  
 2        universal serial bus data.

1        12.   The apparatus of claim 8, wherein said data bus wire carries  
 2        IEEE-1394 bus data.

1        13.   The apparatus of claim 8, further comprising a second  
 2        resistor with a first end and a second end, said first end coupled to said  
 3        data bus wire.

1        14.   The method of claim 13, wherein said second end of said  
 2        second resistor is coupled to signal ground.

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1 15. An apparatus, comprising:  
2 means for providing a first resistor with a first end and a second  
3 end, said first end coupled to a switch and said second end  
4 coupled to a data bus wire;  
5 means for controlling said switch with a detach control signal; and  
6 means for switching a biasing voltage from said resistor utilizing  
7 said switch.

1 16. ~~The method of claim 15, wherein said first resistor is~~  
2 ~~configured as a pull-down resistor.~~

1 17. The method of claim 15, further comprising  
2 means for detecting said switching of said biasing voltage.

1 18. The method of claim 15, wherein said detach control signal  
2 ~~is responsive to a wake-up signal.~~

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